**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

****

**CAD FOR VLSI DESIGN**

**Smart Signature Verification**

**Project report**

**Submitted to, Submitted by,**

**Dr. Harpreet Vohra Pratibha Singh**

**Asst. Professor 602162015**

**M.Tech (VLSI Design)**

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

****

**CAD FOR VLSI DESIGN**

**Smart Signature Verification**

**Project report**

**Submitted to, Submitted by,**

**Dr. Harpreet Vohra T Raja Aadhithan**

**Asst. Professor 602162021**

**M.Tech (VLSI Design)**