**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

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**Analog IC Design**

**Experiment 1**

**Submitted to, Submitted by,**

**Dr. Arun Kumar Pratibha Singh**

**Asst. Professor 602162015**

**M.Tech (VLSI Design)**

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

****

**Analog IC Design**

**Experiment 1**

**Submitted to, Submitted by,**

**Dr. Arun Kumar T Raja Aadhithan**

**Asst. Professor 602162021**

**M.Tech (VLSI Design)**

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

****

**Analog IC Design**

**Experiment 2**

**Submitted to, Submitted by,**

**Dr. Arun Kumar Pratibha Singh**

**Asst. Professor 602162015**

**M.Tech (VLSI Design)**

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

****

**Analog IC Design**

**Experiment 2**

**Submitted to, Submitted by,**

**Dr. Arun Kumar T Raja Aadhithan**

**Asst. Professor 602162021**

**M.Tech (VLSI Design)**

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

****

**Analog IC Design**

**Experiment 3**

**Submitted to, Submitted by,**

**Dr. Arun Kumar Pratibha Singh**

**Asst. Professor 602162015**

**M.Tech (VLSI Design)**

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

****

**Analog IC Design**

**Experiment 3**

**Submitted to, Submitted by,**

**Dr. Arun Kumar T Raja Aadhithan**

**Asst. Professor 602162021**

**M.Tech (VLSI Design)**

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

****

**Analog IC Design**

**Experiment 4**

**Submitted to, Submitted by,**

**Dr. Arun Kumar Pratibha Singh**

**Asst. Professor 602162015**

**M.Tech (VLSI Design)**

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

****

**Analog IC Design**

**Experiment 4**

**Submitted to, Submitted by,**

**Dr. Arun Kumar T Raja Aadhithan**

**Asst. Professor 602162021**

**M.Tech (VLSI Design)**

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

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**Analog IC Design**

**Experiment 5**

**Submitted to, Submitted by,**

**Dr. Arun Kumar Pratibha Singh**

**Asst. Professor 602162015**

**M.Tech (VLSI Design)**

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

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**Analog IC Design**

**Experiment 5**

**Submitted to, Submitted by,**

**Dr. Arun Kumar T Raja Aadhithan**

**Asst. Professor 602162021**

**M.Tech (VLSI Design)**